

## Indian Institute of Technology Gandhinagar Palaj, Gandhinagar Gujarat – 382355

24/01/2017

## Advertisement

Applications are invited from bright, young candidates for the following post under a DST funded project in Discipline of Electrical Engineering.

Name of Post : Research Associate (RA)

Consolidated

: Rs. 15,000/-

Qualification and B. E./B. Tech. in Electrical/Electronics and related disciplines with a valid GATE

Experience score and knowledge in Semiconductor Devices or Microelectronics or equivalent.

Working knowledge of Technology CAD tools like Synopsis Sentaurus/Silivaco is

required. Experience in LINUX operating system is desired.

Tenure : Six months extendable up to three years on satisfactory performance

Eligible candidates may send their bio-data to Dr. Nihar Ranjan Mohapatra, Department of Electrical

Engineering, IIT Gandhinagar, by e-mail (nihar@iitgn.ac.in) on or before 31st of January 2017. Selection

will be based upon interview and shortlisted candidates will be intimated well in advance the date and time of the interview.